

INTEGRATED CIRCUIT DESIGN SYSTEM

Appl. No. : 10/710,905 Confirmation No.: 4904
Applicant : Chien-Jung Hsin,
Jun-Jyeh Hsiao,
Sheng-Chun Lee
Filed : August 12, 2004
TC/A.U. : 2825
Examiner : MEMULA, SURESH
Docket No. : DROP0001USA
Customer No. : 27765

Commissioner for Patents
P.O. Box 1450
Alexandria VA 22313-1450

AMENDMENT

Sir:

- 5 In response to the Office action of December 20, 2006, please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 4 of this paper.